

## CLAIMS

What is claimed is:

1. A method comprising:

- 2 requesting a first deferred procedure call for a first interrupt event;  
3 requesting at least one other deferred procedure call for a second interrupt event;  
4 assigning the first deferred procedure call and the at least one other deferred procedure  
5 call to a resource;  
6 processing the first interrupt event with the first deferred procedure call; and  
7 processing the second interrupt event with the at least one other deferred procedure call.

2. The method of claim 1, further comprising:

- 2 assigning the first deferred procedure call and the at least one other deferred procedure  
3 call to a resource comprising a processor exhibiting a single thread of execution;  
4 and  
5 executing the first deferred procedure call and the at least one other deferred procedure  
6 call on the single thread.

3. The method of claim 1, further comprising:

- 2 assigning the first deferred procedure call and the at least one other deferred procedure  
3 call to a resource comprising a processor exhibiting a plurality of threads; and  
4 executing the first deferred procedure call on one thread of the plurality of threads while  
5 executing the at least one other deferred procedure call on another thread of the  
6 plurality of threads.

1           4.     The method of claim 1, further comprising:  
2     assigning the first deferred procedure call to a resource comprising a first thread of a  
3         processor;  
4     assigning the at least one other deferred procedure call to a resource comprising a second  
5         thread of the processor; and  
6     executing the first deferred procedure call on the first thread while executing the at least  
7         one other deferred procedure call on the second thread.

1           5.     The method of claim 1, further comprising:  
2     assigning the first deferred procedure call and the at least one other deferred procedure  
3         call to a resource comprising a multi-processor system; and  
4     executing the first deferred procedure call on one processor of the multi-processor system  
5         while executing the at least one other deferred procedure call on another processor  
6         of the multi-processor system.

1           6.     The method of claim 1, further comprising:  
2     assigning the first deferred procedure call to a resource comprising a first processor;  
3     assigning the at least one other deferred procedure call to a resource comprising a second  
4         processor; and  
5     executing the first deferred procedure call on the first processor while executing the at  
6         least one other deferred procedure call on the second processor.

1           7.     The method of claim 1, further comprising processing another interrupt  
2     event with one of the first deferred procedure call and the at least one other deferred  
3     procedure call.

1           8.     A method comprising:  
2     requesting a first deferred procedure call for a first interrupt event;  
3     requesting at least one other deferred procedure call for a second interrupt event; and  
4     processing the first interrupt event with the first deferred procedure call while processing  
5     the second interrupt event with the at least one other deferred procedure call.

1           9.     The method of claim 8, further comprising:  
2     executing the first deferred procedure call on a first thread of a processor; and  
3     executing the at least one other deferred procedure call on a second thread of the  
4     processor.

1           10.    The method of claim 8, further comprising:  
2     executing the first deferred procedure call on a first processor; and  
3     executing the at least one other deferred procedure call on a second processor.

1           11.    The method of claim 8, further comprising processing another interrupt  
2     event with one of the first deferred procedure call and the at least one other deferred  
3     procedure call.

1           12.    A driver comprising:  
2     an interrupt handler to identify interrupt events; and  
3     at least two deferred procedure calls, each of the at least two deferred procedure calls to  
4     process at least one of the interrupt events.

1           13.    The driver of claim 12, the interrupt handler to assign the at least two  
2     deferred procedure calls to a resource for execution.

1           14.    The driver of claim 12, the interrupt handler to assign one of the at least  
2     two deferred procedure calls to a first resource for execution and another of the at least  
3     two deferred procedure calls to a second resource for execution.

1           15.     A computer system comprising:  
2     a driver stored in a memory of the computer system, the driver including  
3           an interrupt handler to identify interrupt events; and  
4           at least two deferred procedure calls, each of the at least two deferred procedure  
5           calls to process at least one of the interrupt events.  
6           and  
7     a processor to execute the at least two deferred procedure calls.

1           16.     The computer system of claim 15, the interrupt handler to assign the at  
2     least two deferred procedure calls to a single thread exhibited by the processor for  
3     execution.

1           17.     The computer system of claim 15, the interrupt handler to assign a first of  
2     the at least two deferred procedure calls to one thread of the processor and another of the  
3     at least two deferred procedure calls to a second thread of the processor for execution.

1           18.     The computer system of claim 15, the interrupt handler to assign one of  
2     the at least two deferred procedure calls to the processor and another of the at least two  
3     deferred procedure calls to a second processor for execution.

1           19.     The computer system of claim 15, further comprising at least one  
2     peripheral device, the interrupt events associated with the at least one peripheral device.

1           20.     An article of manufacture comprising:  
2     a machine accessible medium, the machine accessible medium providing instructions  
3           that, when executed by a machine, cause the machine to:  
4           request a first deferred procedure call for a first interrupt event;  
5           request at least one other deferred procedure call for a second interrupt event;  
6           assign the first deferred procedure call and the at least one other deferred  
7                 procedure call to a resource;  
8           process the first interrupt event with the first deferred procedure call; and  
9           process the second interrupt event with the at least one other deferred procedure  
10           call.

1           21.     The article of claim 20, wherein the instructions, when executed, further  
2     cause the machine to:  
3     assign the first deferred procedure call and the at least one other deferred procedure call  
4           to a resource comprising a processor exhibiting a single thread of execution; and  
5     execute the first deferred procedure call and the at least one other deferred procedure call  
6           on the single thread.

1           22.     The article of claim 20, wherein the instructions, when executed, further  
2     cause the machine to:  
3     assign the first deferred procedure call and the at least one other deferred procedure call  
4           to a resource comprising a processor exhibiting a plurality of threads; and  
5     execute the first deferred procedure call on one thread of the plurality of threads while  
6           executing the at least one other deferred procedure call on another thread of the  
7           plurality of threads.

1           23.     The article of claim 20, wherein the instructions, when executed, further  
2     cause the machine to:  
3     assign the first deferred procedure call to a resource comprising a first thread of a  
4         processor;  
5     assign the at least one other deferred procedure call to a resource comprising a second  
6         thread of the processor; and  
7     execute the first deferred procedure call on the first thread while executing the at least  
8         one other deferred procedure call on the second thread thread.

1           24.     The article of claim 20, wherein the instructions, when executed, further  
2     cause the machine to:  
3     assign the first deferred procedure call and the at least one other deferred procedure call  
4         to a resource comprising a multi-processor system; and  
5     execute the first deferred procedure call on one processor of the multi-processor system  
6         while executing the at least one other deferred procedure call on another processor  
7         of the multi-processor system.

1           25.     The article of claim 20, wherein the instructions, when executed, further  
2     cause the machine to:  
3     assign the first deferred procedure call to a resource comprising a first processor;  
4     assign the at least one other deferred procedure call to a resource comprising a second  
5         processor; and  
6     execute the first deferred procedure call on the first processor while executing the at least  
7         one other deferred procedure call on the second processor.

1           26.     The article of claim 20, wherein the instructions, when executed, further  
2     cause the machine to process another interrupt event with one of the first deferred  
3     procedure call and the at least one other deferred procedure call.

1           27.     An article of manufacture comprising:  
2     a machine accessible medium, the machine accessible medium providing instructions  
3           that, when executed by a machine, cause the machine to:  
4           request a first deferred procedure call for a first interrupt event;  
5           request at least one other deferred procedure call for a second interrupt event; and  
6           process the first interrupt event with the first deferred procedure call while  
7                   processing the second interrupt event with the at least one other deferred  
8                   procedure call.

1           28.     The article of claim 27, wherein the instructions, when executed, further  
2     cause the machine to:  
3     execute the first deferred procedure call on a first thread of a processor; and  
4     execute the at least one other deferred procedure call on a second thread of the processor.

1           29.     The article of claim 27, wherein the instructions, when executed, further  
2     cause the machine to:  
3     execute the first deferred procedure call on a first processor; and  
4     execute the at least one other deferred procedure call on a second processor.

1           30.     The article of claim 27, wherein the instructions, when executed, further  
2     cause the machine to process another interrupt event with one of the first deferred  
3     procedure call and the at least one other deferred procedure call.

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